



02/05/01

398	137	Subclass	ISSUE CLASSIFICATION
		Class	

PATENT NUMBER

U.S. ~~UTILITY~~ Patent Application

O.I.P.E. AK SCANNED <u>MS 4</u> Q.A. <u>AA</u>	PATENT DATE
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APPLICATION NO. 09/777917	CONT/PRIOR	CLASS 359	SUBCLASS <del>45</del> 451	ART UNIT 2633	EXAMINER <i>Leung</i>
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## APPLICANTS


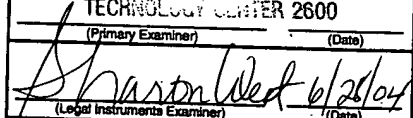
Lewis Aronson  
Stephen Hosking

# TITLE

Integrated memory mapped controller circuit for fiber optics transceiver

ISSUING CLASSIFICATION											
ORIGINAL				CROSS REFERENCE(S)							
CLASS		SUBCLASS		CLASS		SUBCLASS (ONE SUBCLASS PER BLOCK)					
398		137		398		135					
INTERNATIONAL CLASSIFICATION											
#048		10/00									

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<input type="checkbox"/> <b>TER DISC</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	<b>Sheets Drwg.</b> 3	<b>Figs. Drwg.</b> 3	<b>Print Fig.</b> 3	<b>Total Claims</b> 77	<b>Print Claim for O.G.</b> 1
	<i>Elizabeth Y. Kemp</i> 6-16-04 <small>(Assistant Examiner)</small> <small>(Date)</small>			<b>NOTICE OF ALLOWANCE MAILED</b>	
	 <b>JASON CHAN</b> <b>SUPERVISORY PATENT EXAMINER</b> <b>TECHNOLOGY CENTER 2600</b> <small>(Primary Examiner)</small> <small>(Date)</small>			5/4/04 07/06/05 <b>ISSUE FEE</b>	
<input type="checkbox"/> The term subsequent to has been dis	<input type="checkbox"/> The term not extend to of U.S. Patent			Amount Due \$ 70 1330.00	Date Paid
<input type="checkbox"/> The term this patent is	 <small>(Legal Instruments Examiner)</small> <small>(Date)</small>			<b>ISSUE BATCH NUMBER</b>	
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